

**REMARKS/ARGUMENTS**

The Applicants have amended Page 1 of the specification by adding a paragraph entitled "Cross-Reference to Related Applications". The Applicants submit that this amendment introduces no new matter.

Claims 1 to 8 stand rejected under 35 USC 103(a) as being unpatentable over Park. With respect, Applicant disagrees. In a previous response to a rejection of the present claims, Applicant demonstrated a point of distinction between the present claims and the teachings of Park. That point of distinction included "*operating non-flashing CMOS structures in the chip, in which pMOS and nMOS transistors are driven such that they do not have intermediate resistance simultaneously during a change of state of the CMOS structure, to manipulate the secret data*".

Subsequently, the Examiner has indicated that this feature is well known in the art. On this point, the Applicant respectfully disagrees. The Applicant considers that a system in which the pMOS and nMOS transistors of a non-flashing CMOS structure are driven such that they do not have intermediate resistance simultaneously during a change of state of the CMOS structure is not a system that would be routinely considered by the person skilled in the art in the context of the invention defined by claim 1. In the absence of any evidence from the Examiner to the contrary, Applicant re-iterates that the invention defined by claim 1 is novel and inventive over Park.

In light of these remarks, further consideration of the application is respectfully requested.

Very respectfully,

Applicants:

  
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